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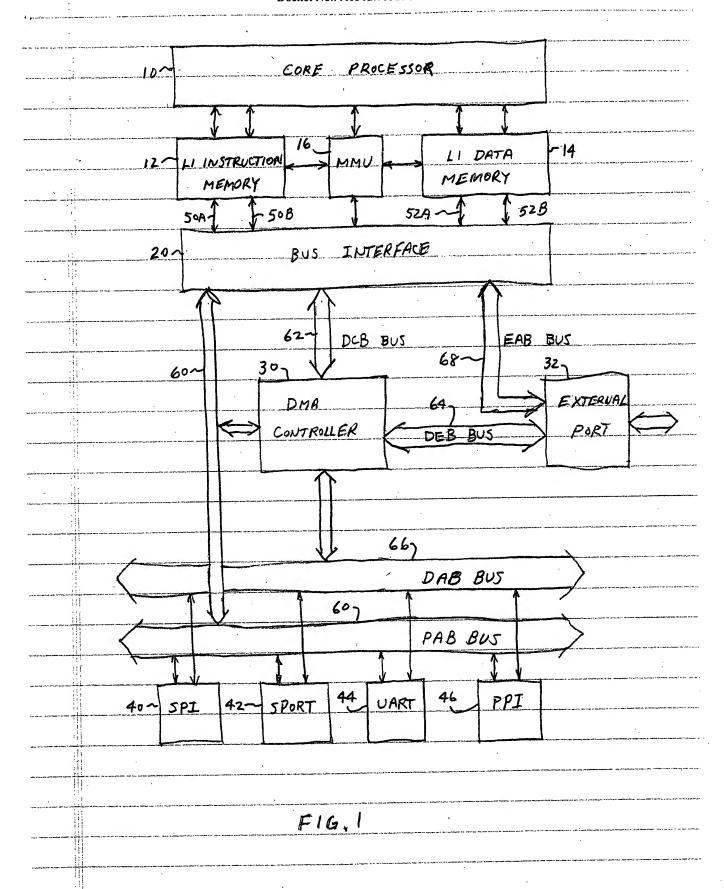
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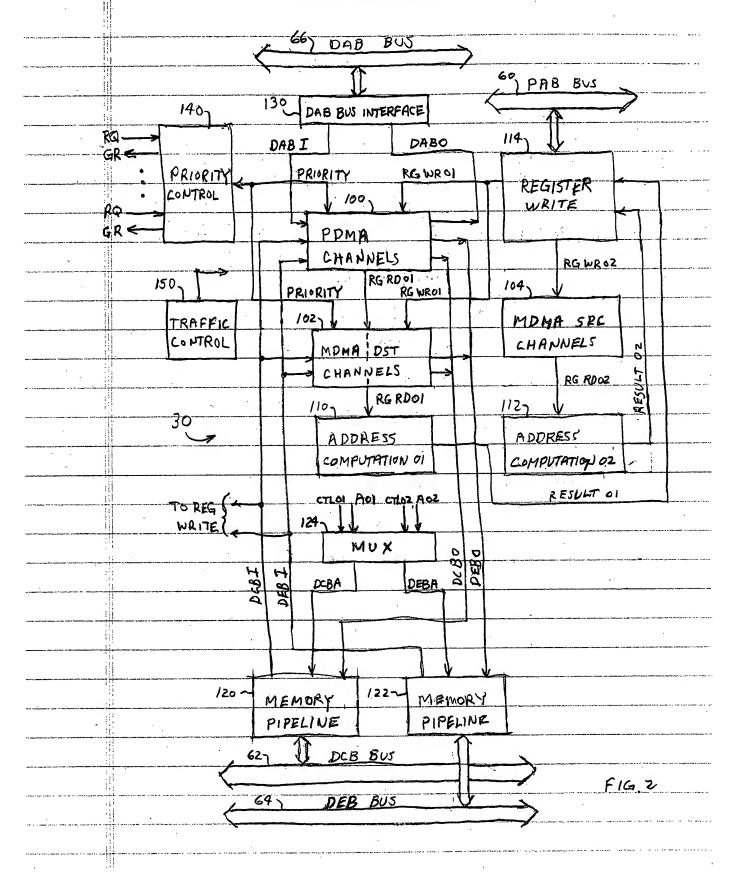
DMA Controller Having Programmable Channel Priority Hayden et al.

Docket No.: A0312.70520US00



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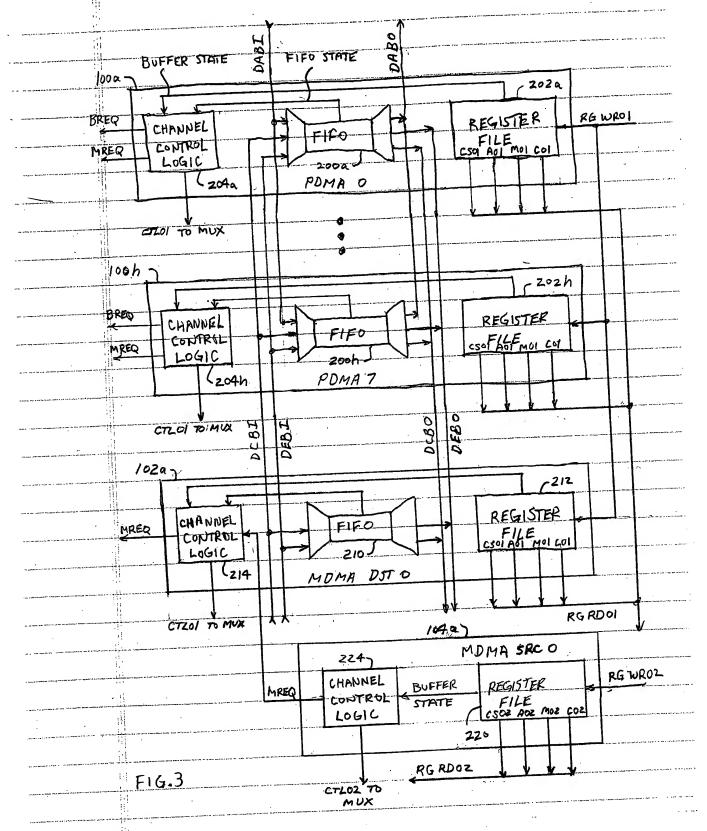
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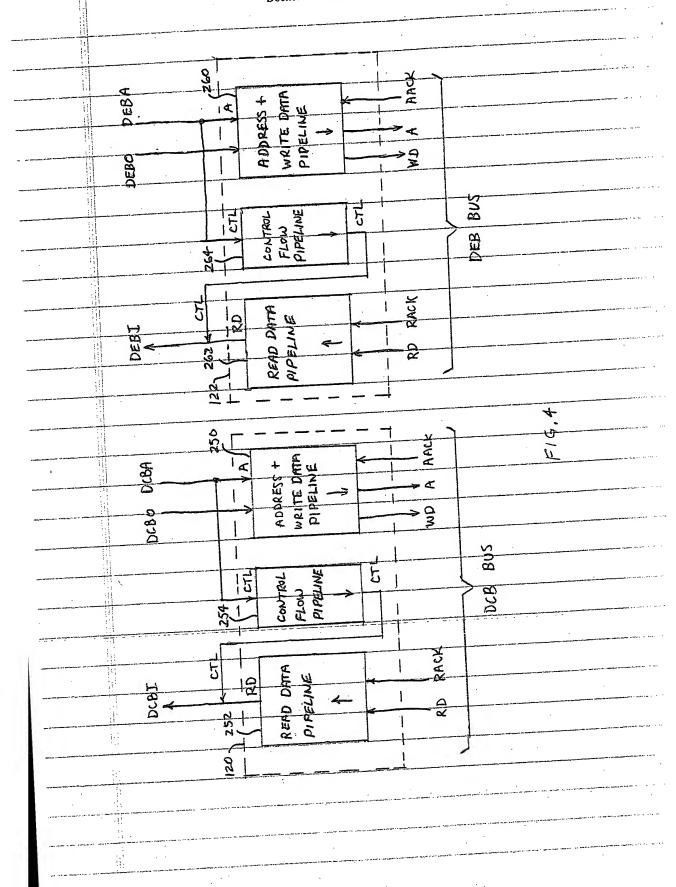


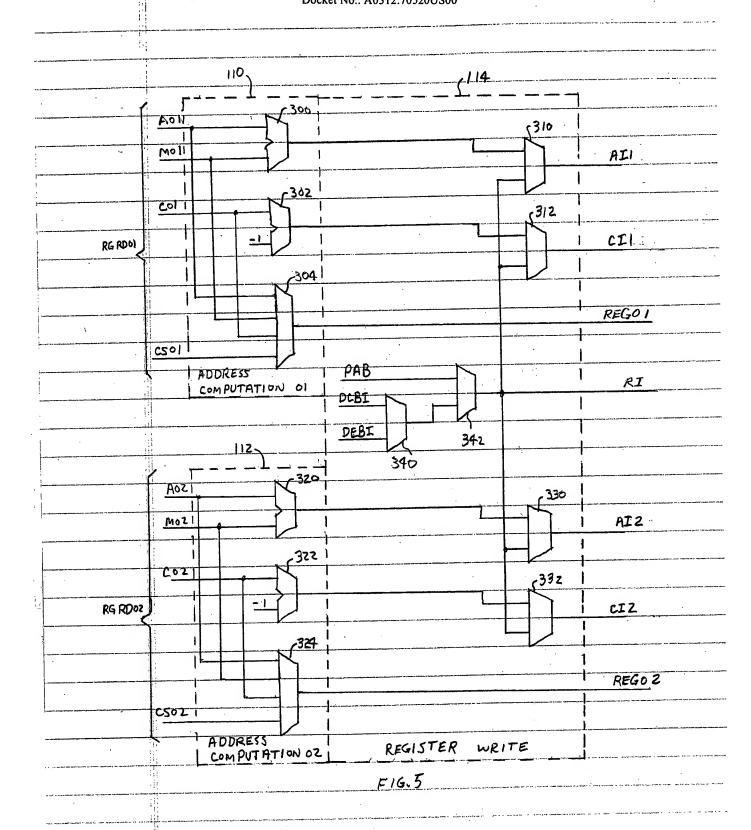
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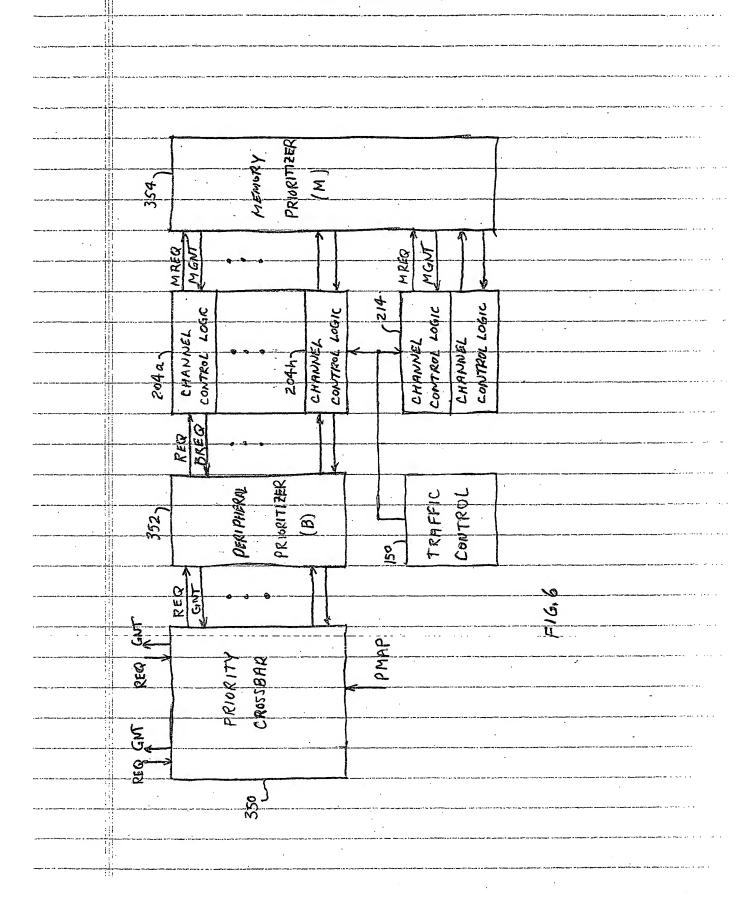
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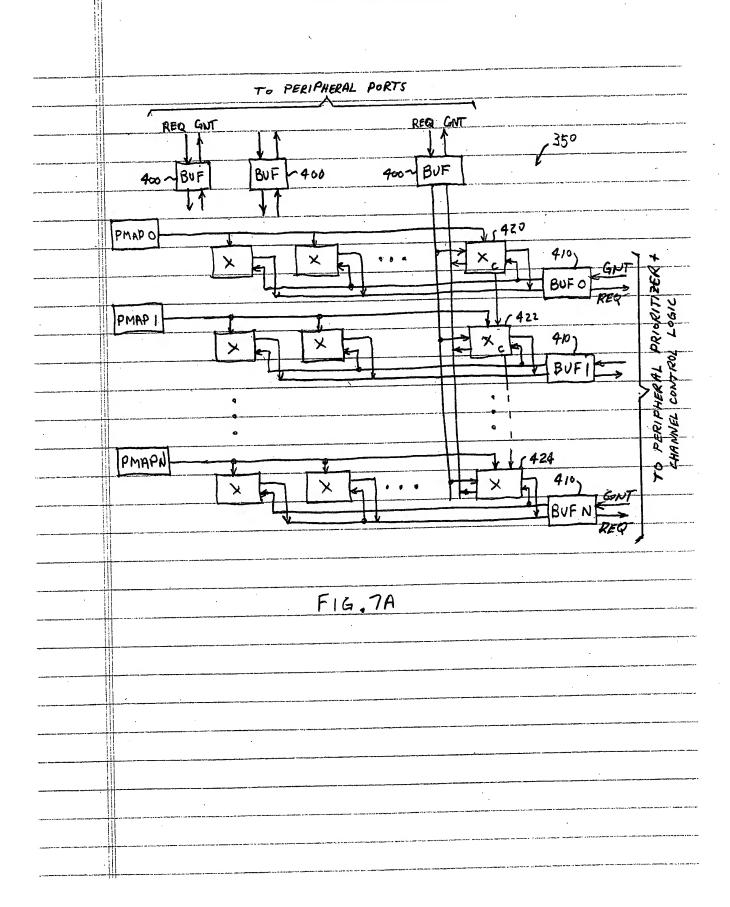
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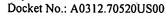


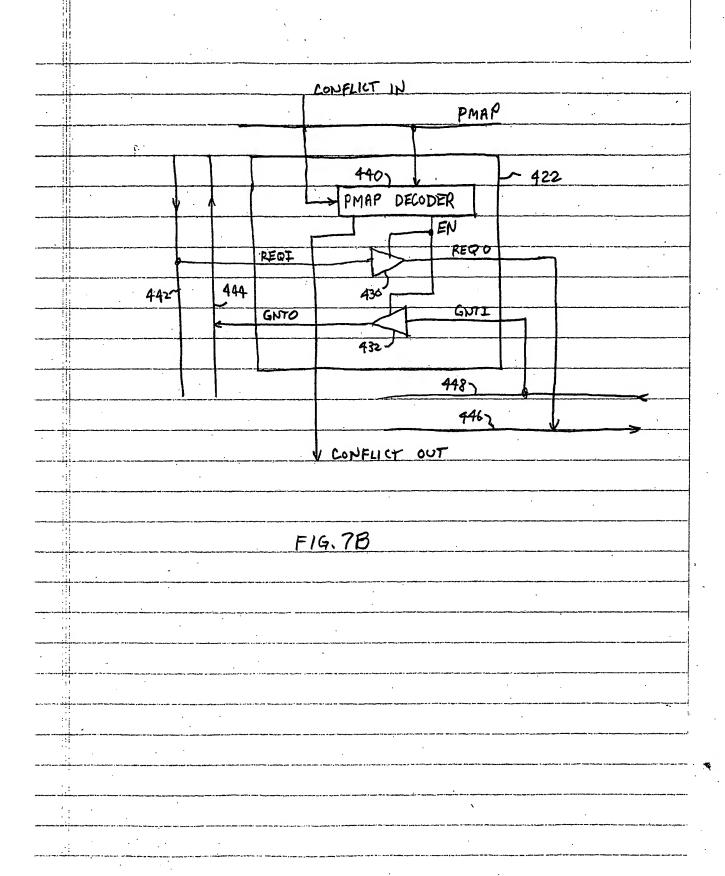




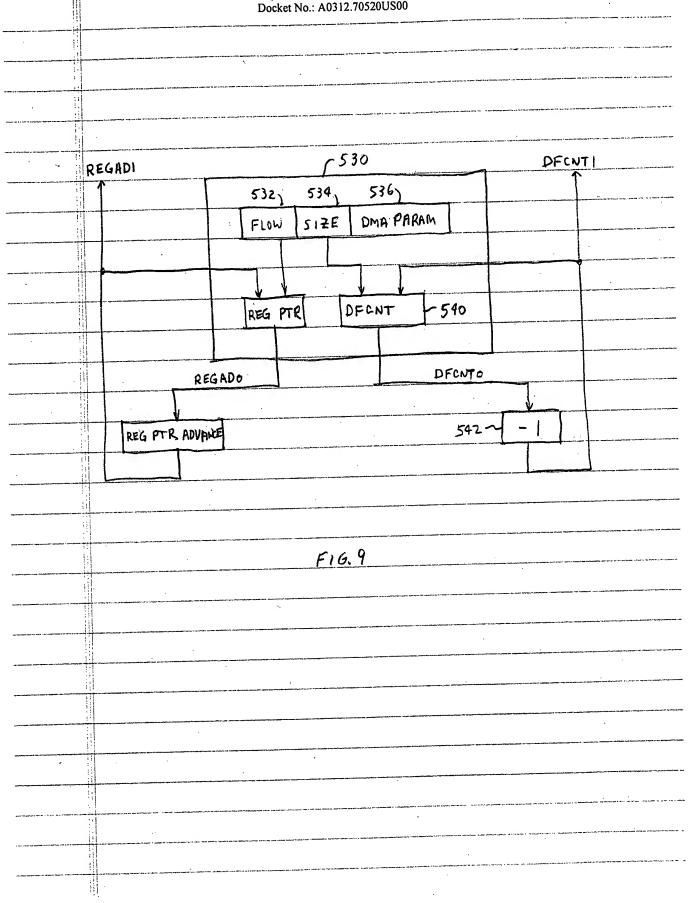


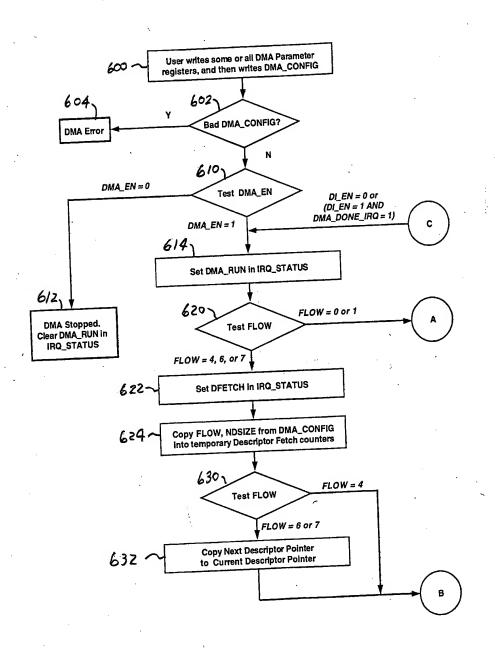




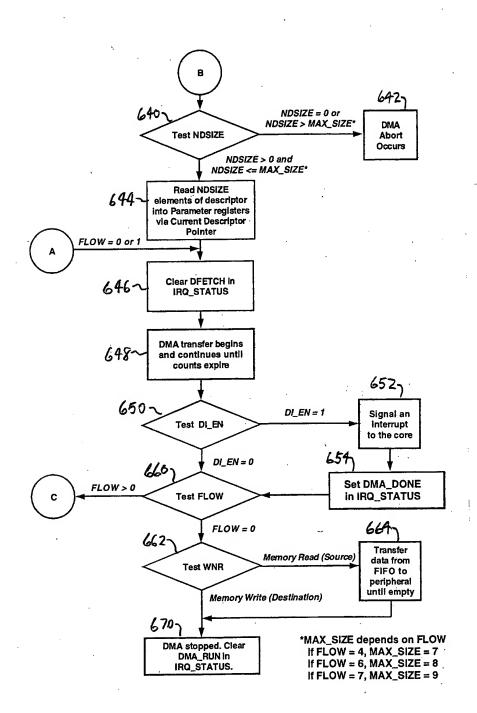


DMA Controller Having Programmable Channel Priority Hayden et al. Docket No.: A0312.70520US00 FLOW = 5TOP ND 5126 = X 35 9 I ND PTR Flow = LIST-L ND SIZE = 6 Òo SIZE 1-121-1 510 ADDR FLOW = ND SIZE 





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